

	
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

**U.S. UTILITY Patent Application**

12W O.I.P.E. 174 SCANNED 175 Q.A. AA	PATENT DATE
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APPLICATION NO. 09/737129	CONT/PRIOR	CLASS <del>704</del> 704	SUBCLASS 1004	ART UNIT <del>1004</del>	EXAMINER <del>1004</del> Ali
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## APPLICANTS

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2127

37415

### Hardware assisted method for scheduling threads using cache locality

PTO-2040  
12/99

ISSUING CLASSIFICATION											
ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION											

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Assistant Examiner) _____ (Date)   _____ (Primary Examiner) _____ (Date)  _____ (Legal Instruments Examiner) _____ (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
			<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.			<b>ISSUE BATCH NUMBER</b>	
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